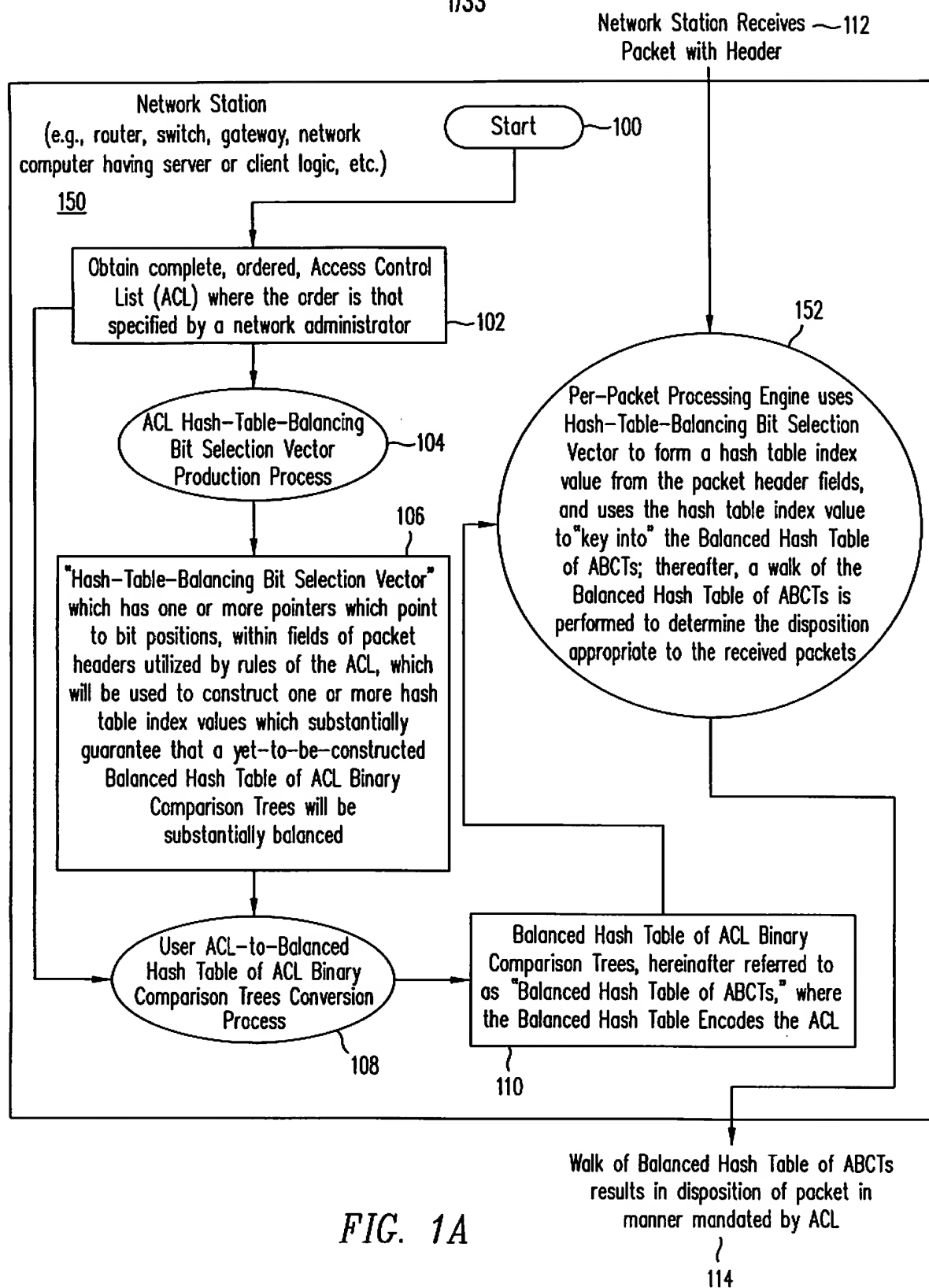
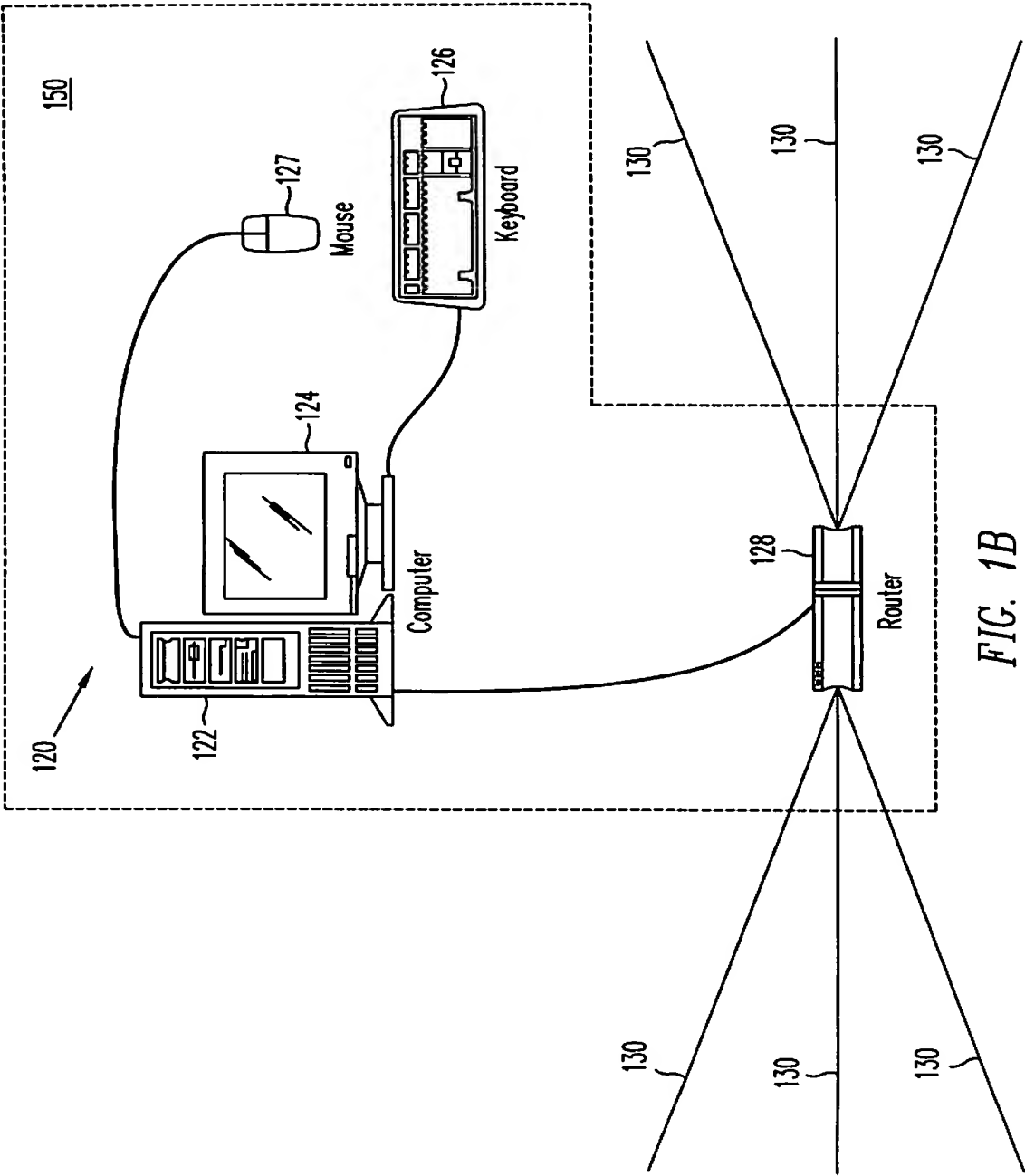


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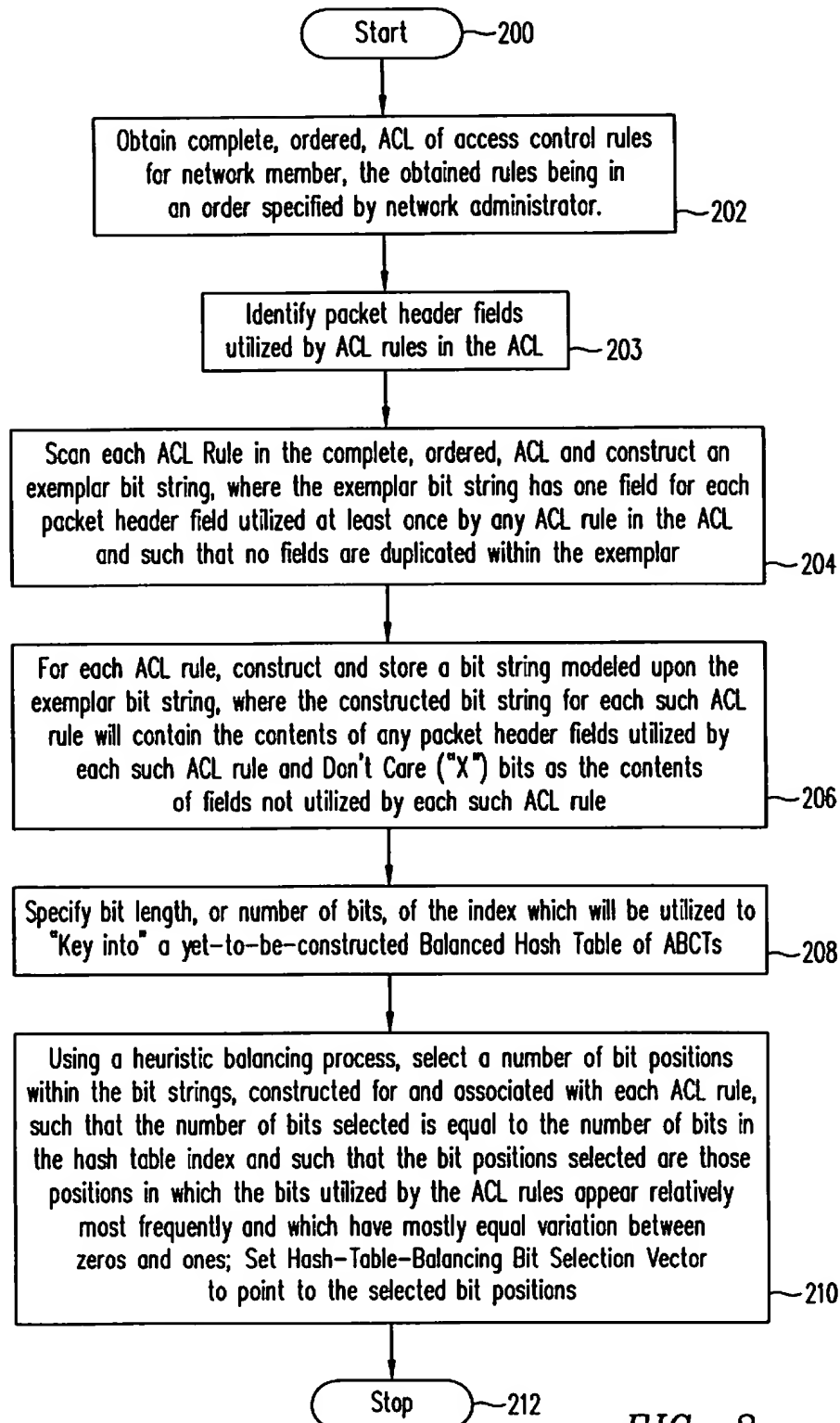


FIG. 2

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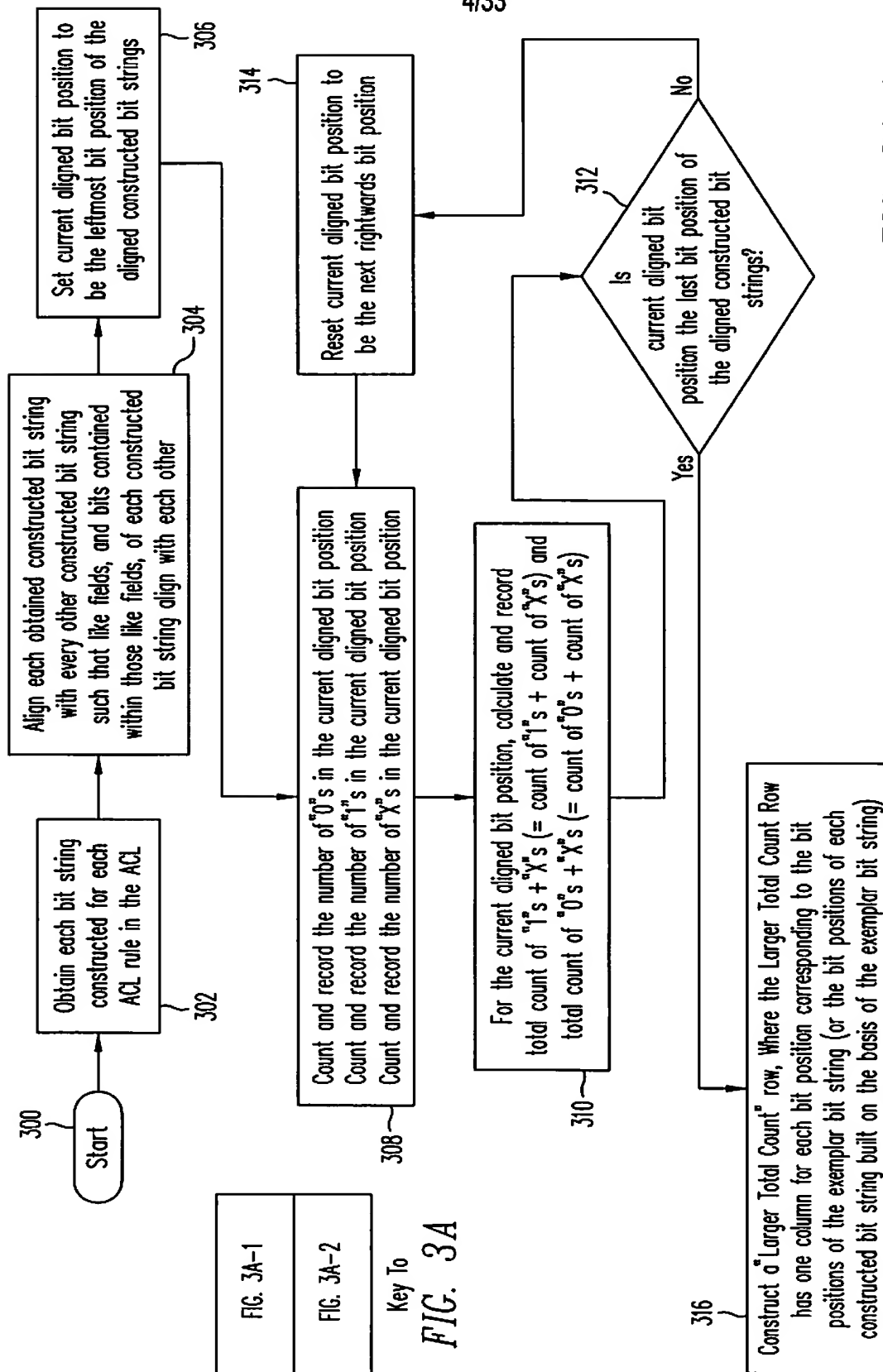
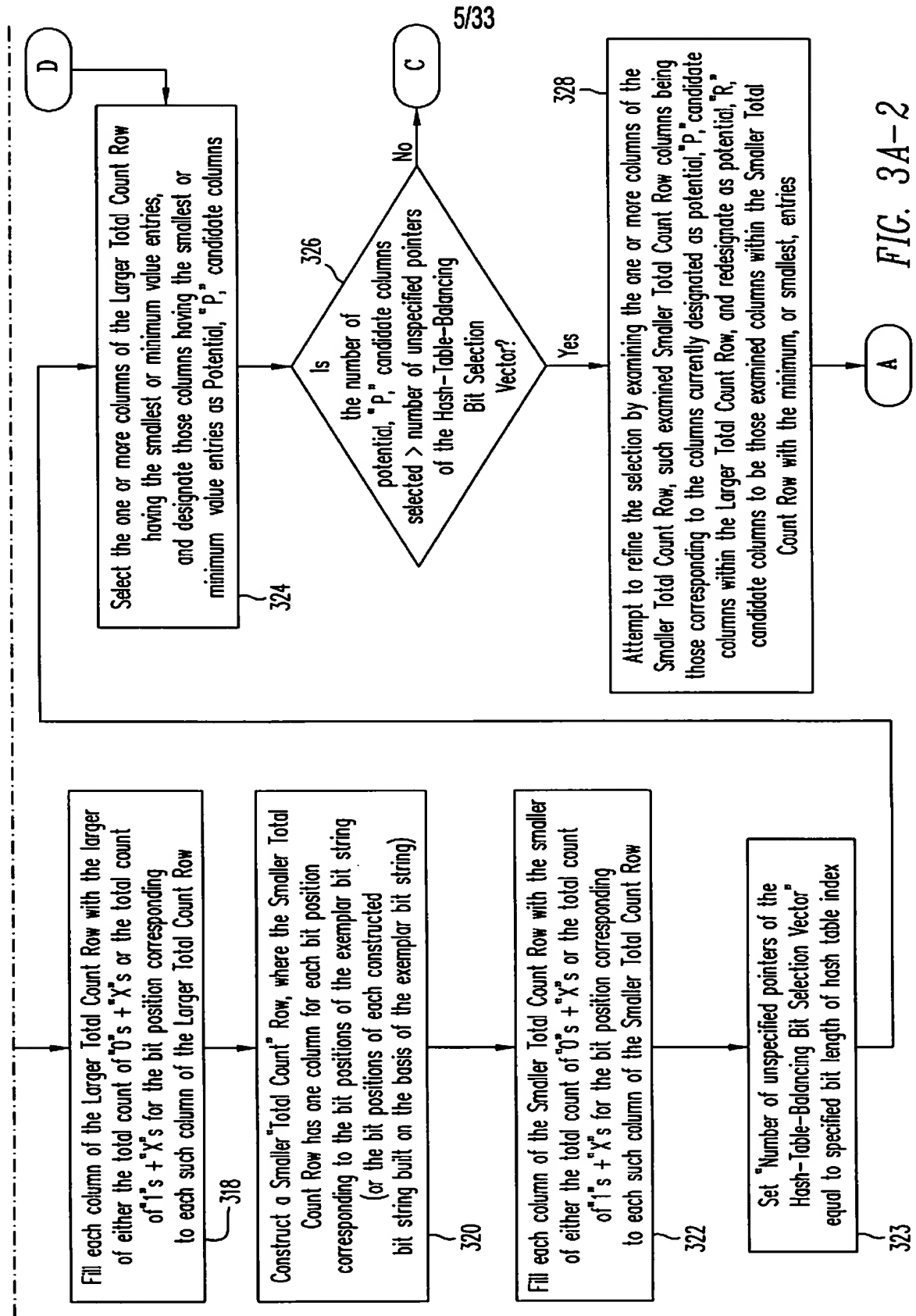
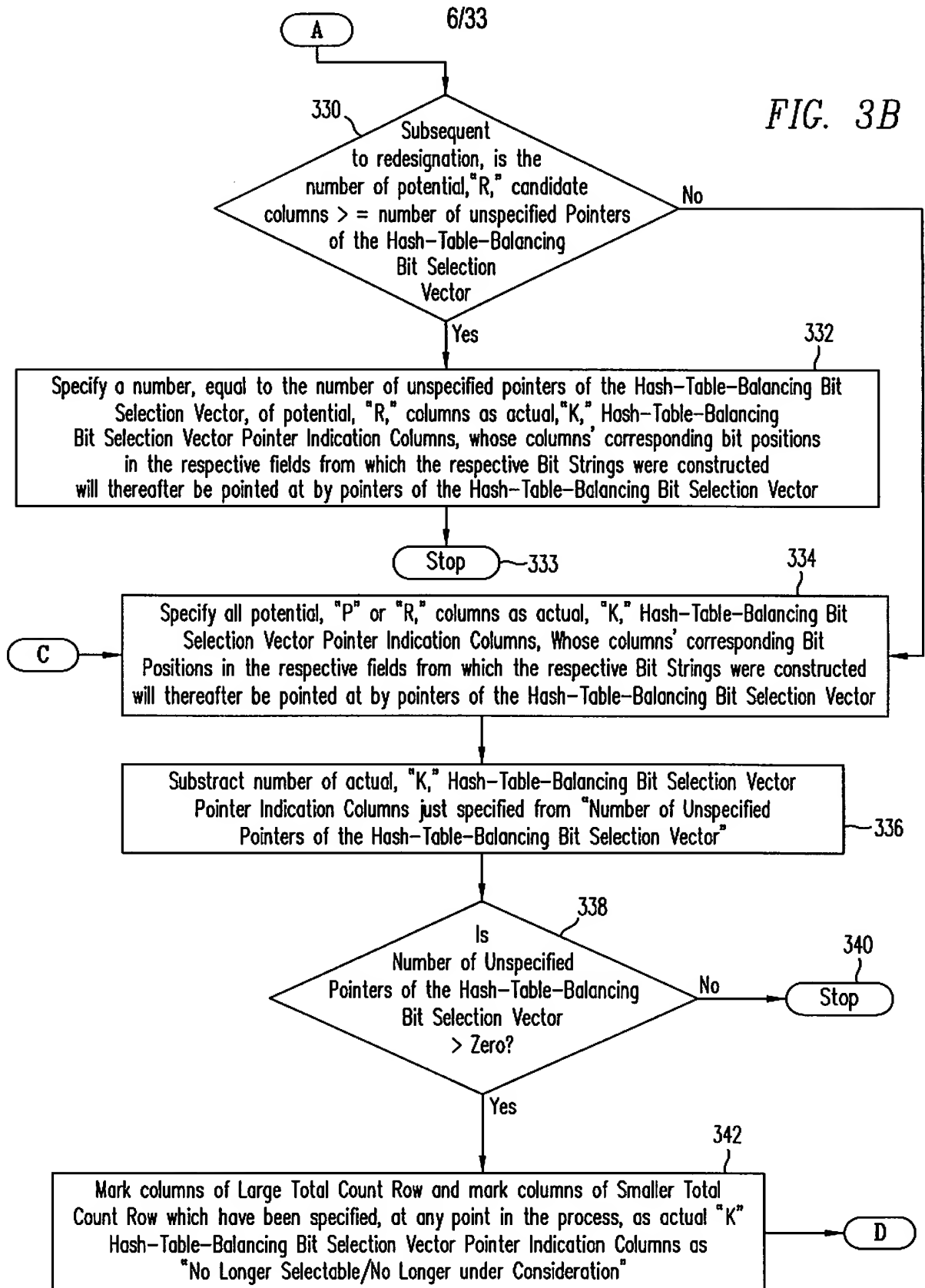
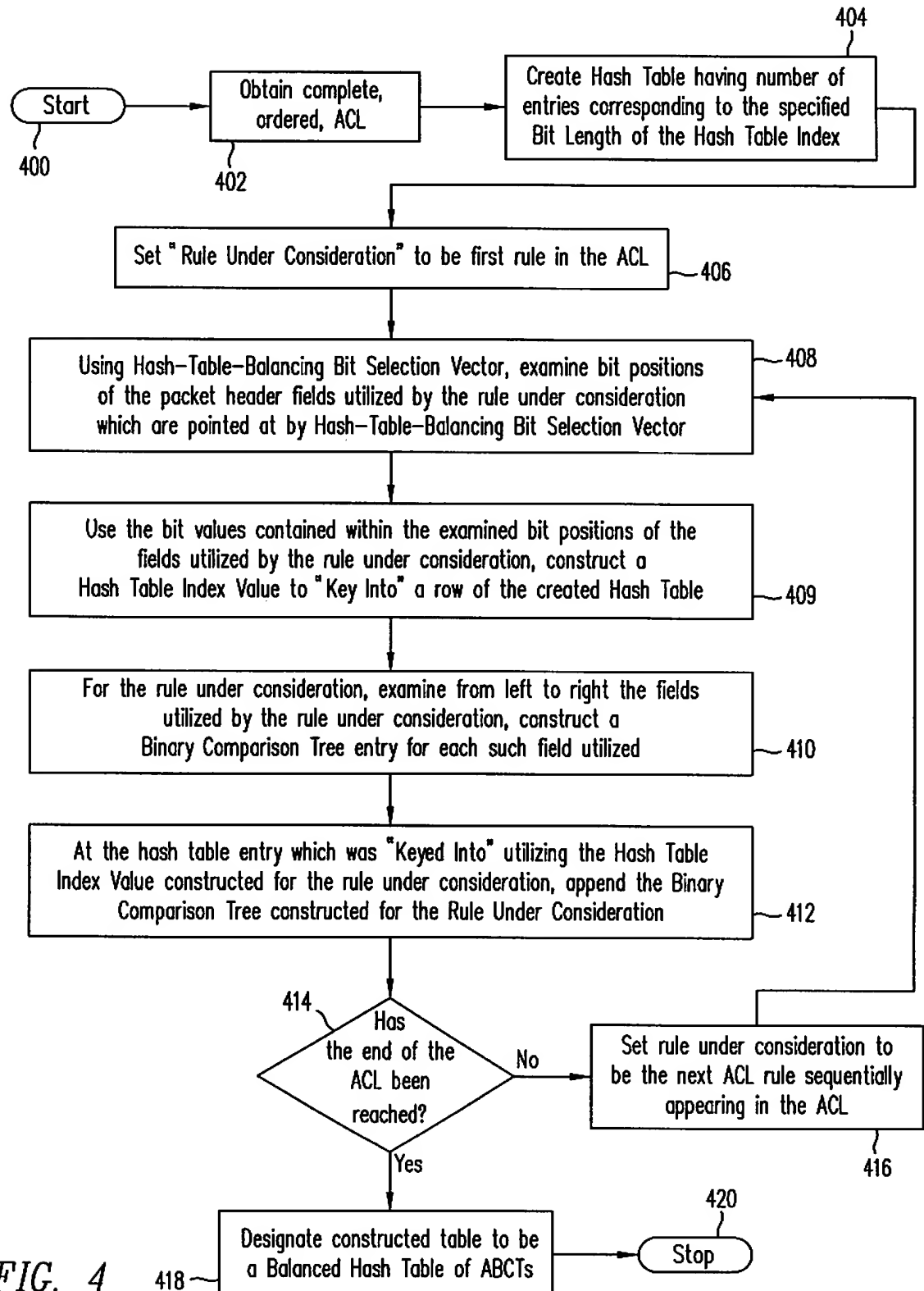


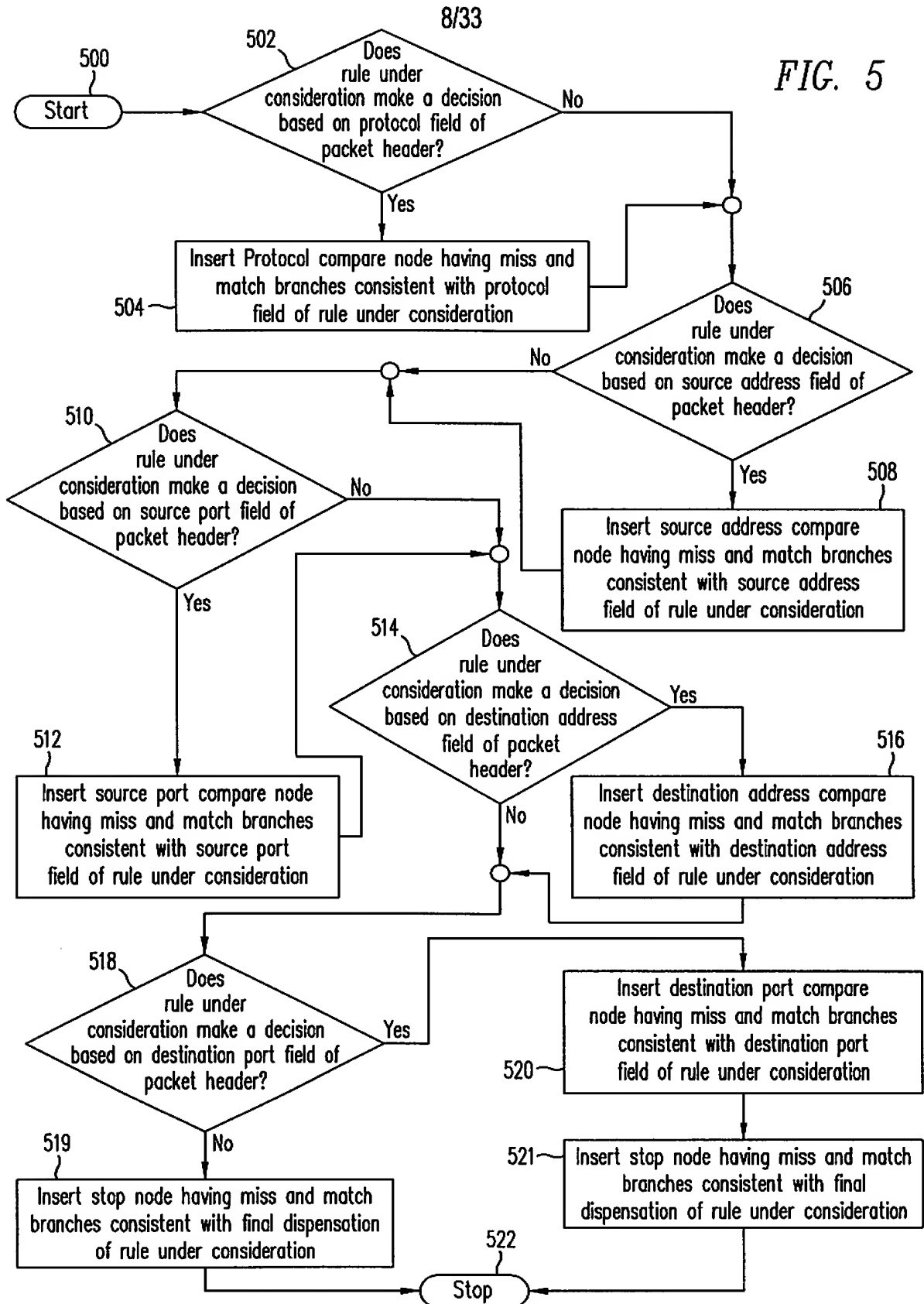
FIG. 3A-1



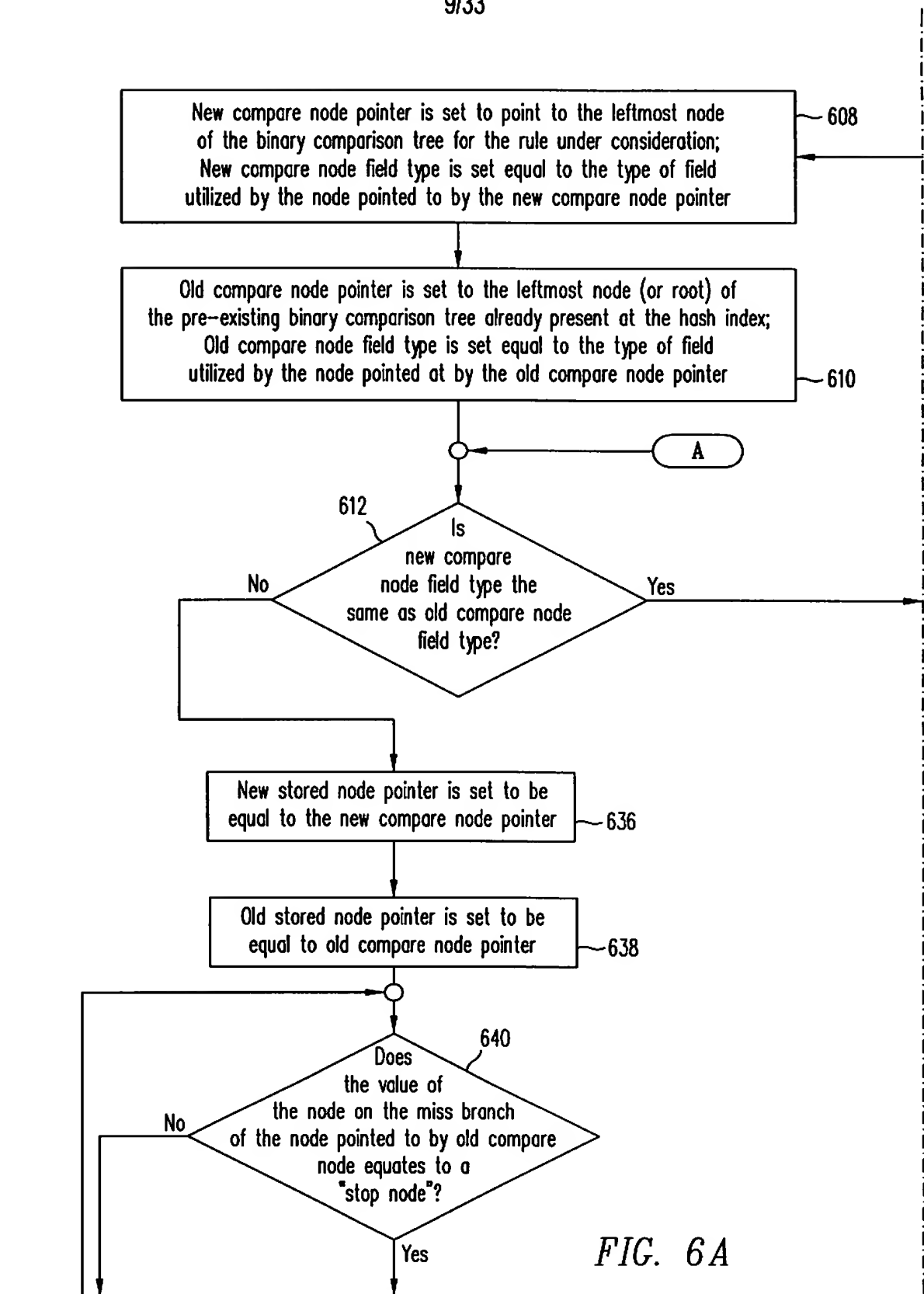


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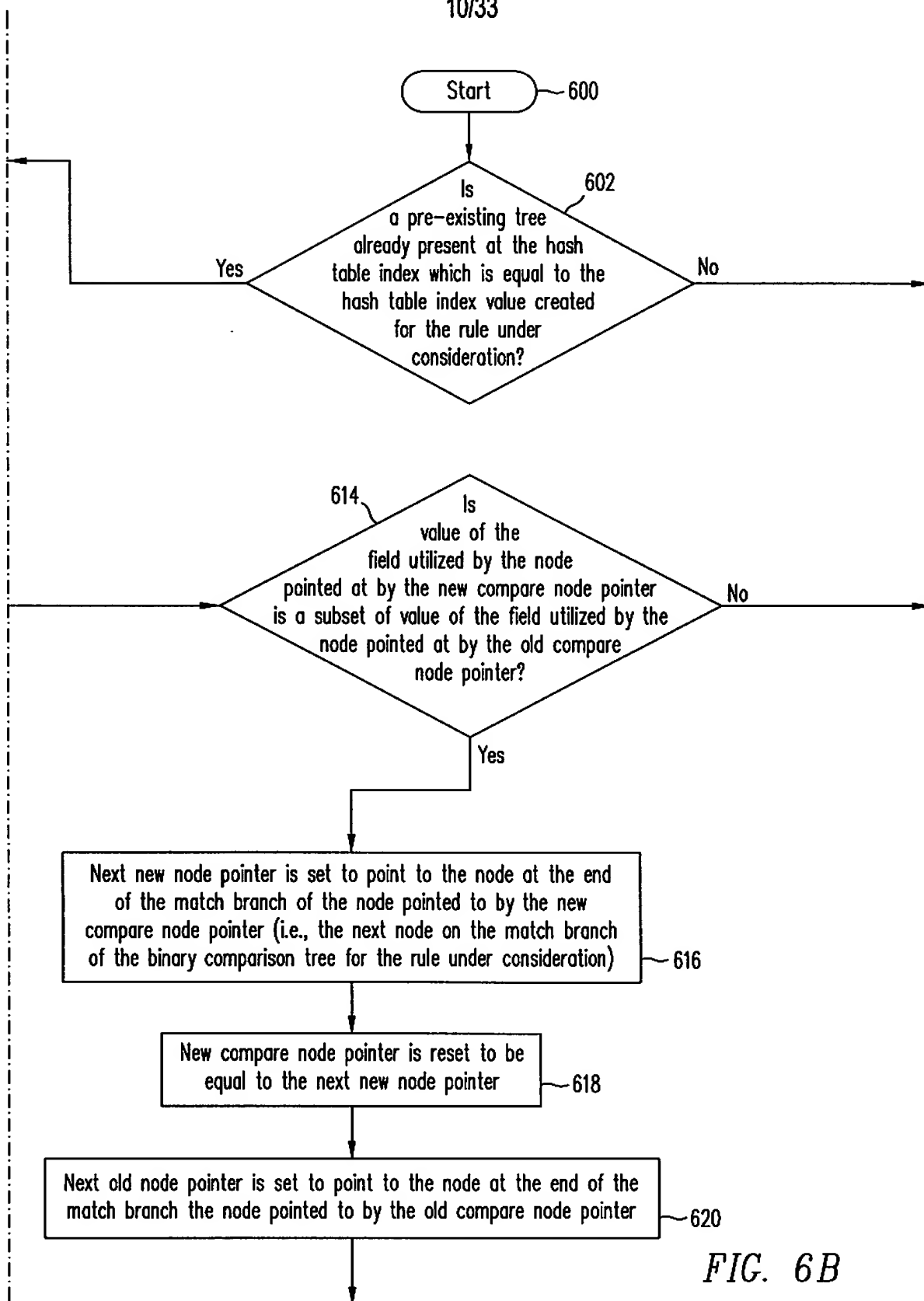




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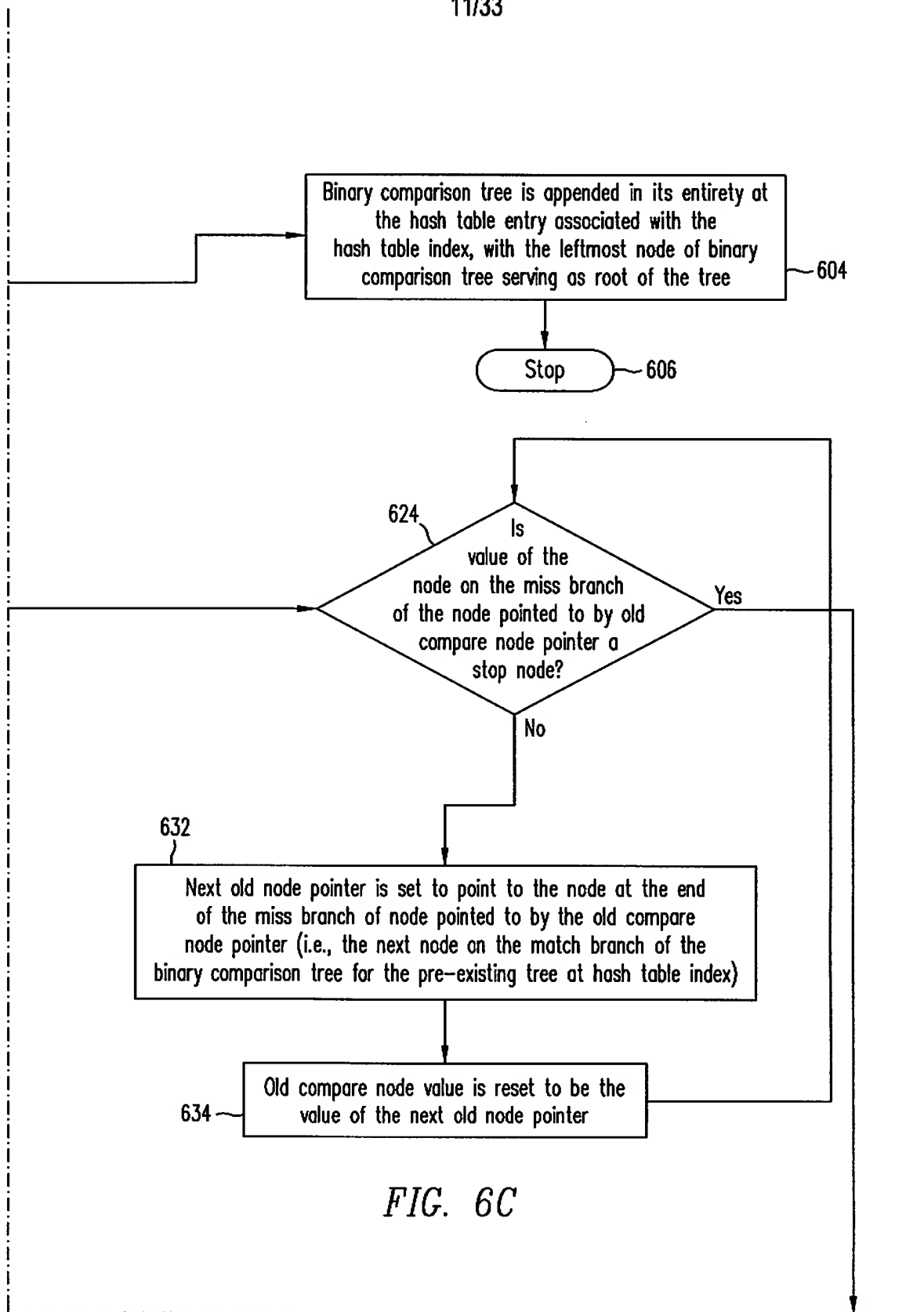


FIG. 6C

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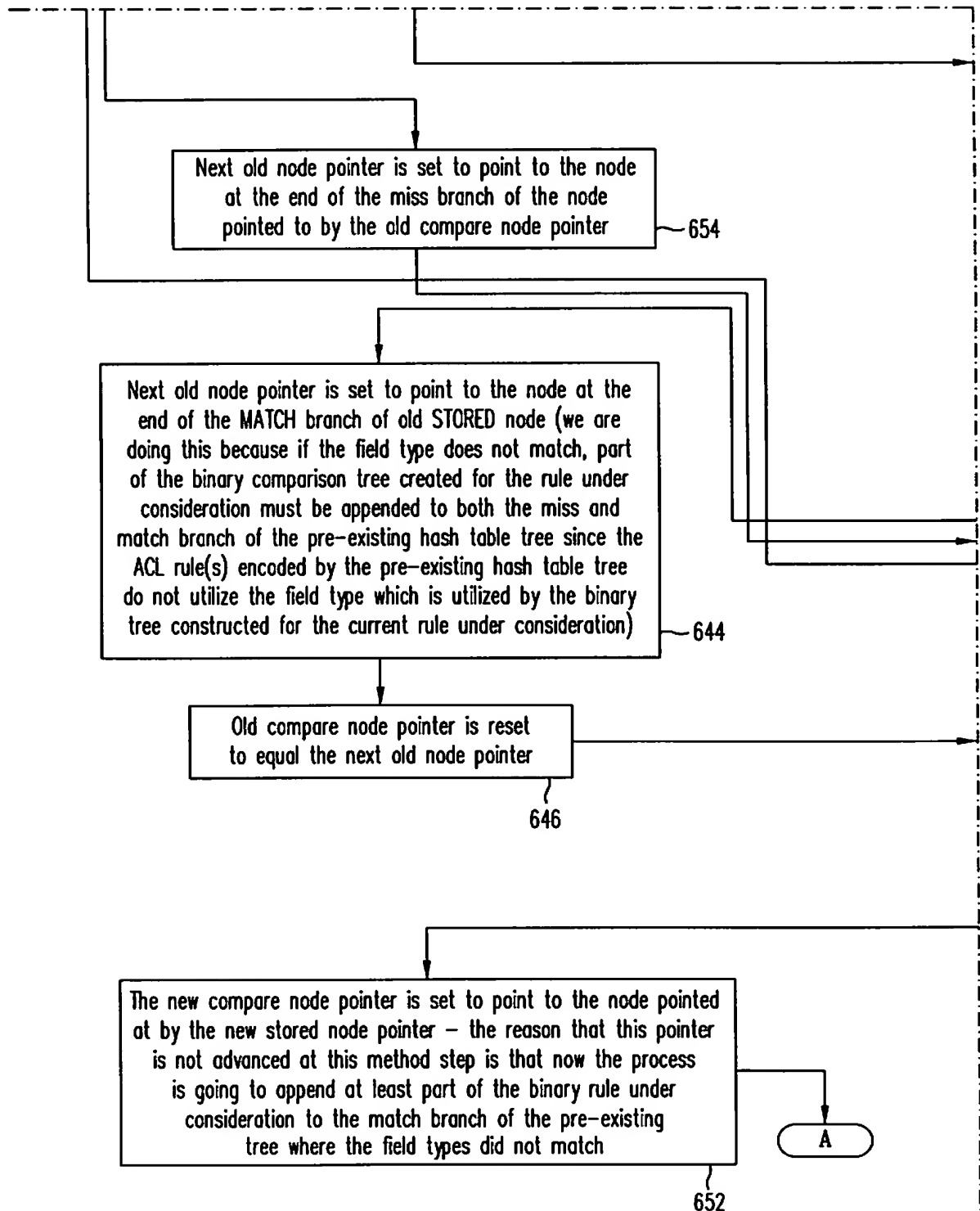


FIG. 6D

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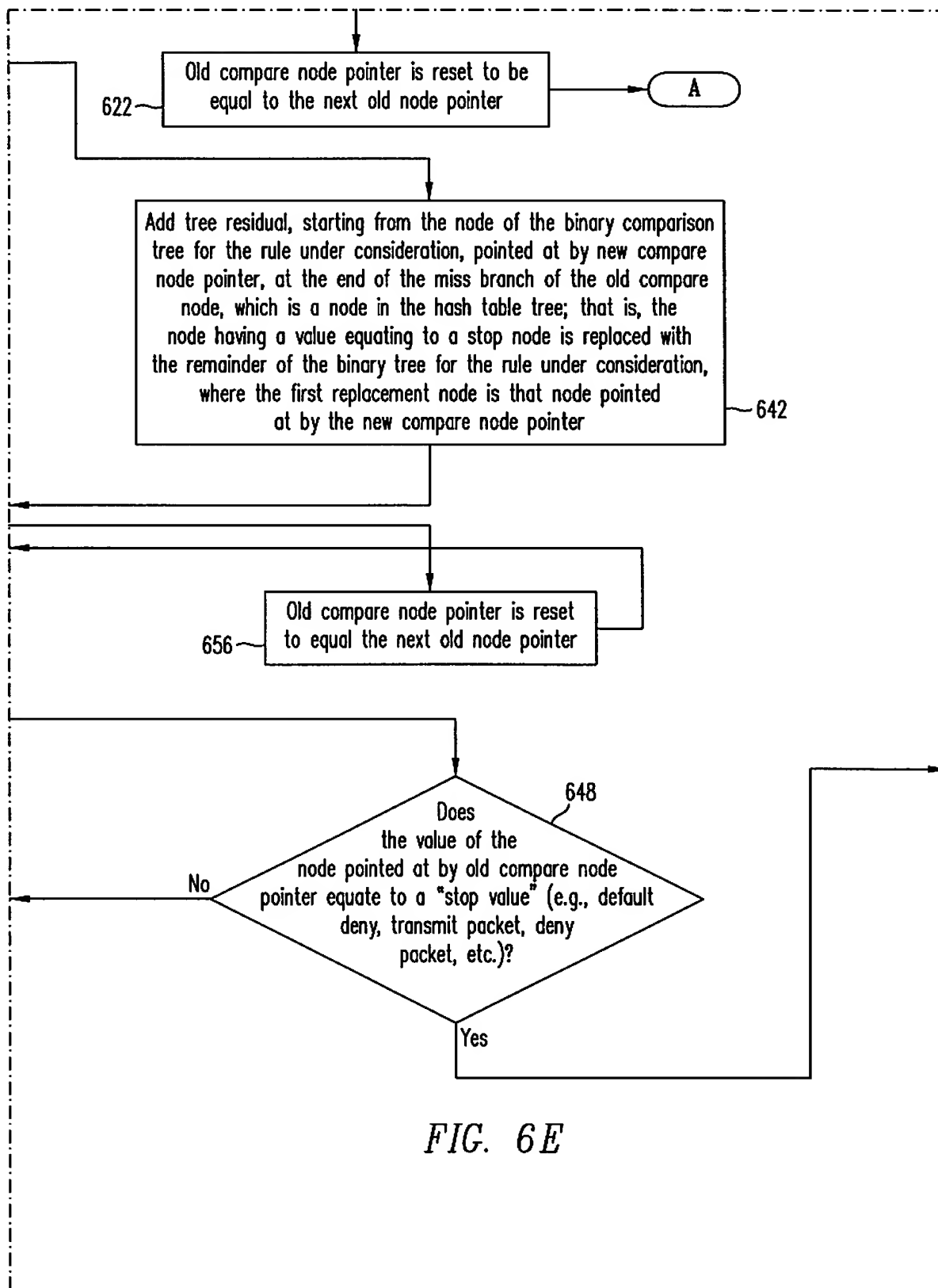


FIG. 6E

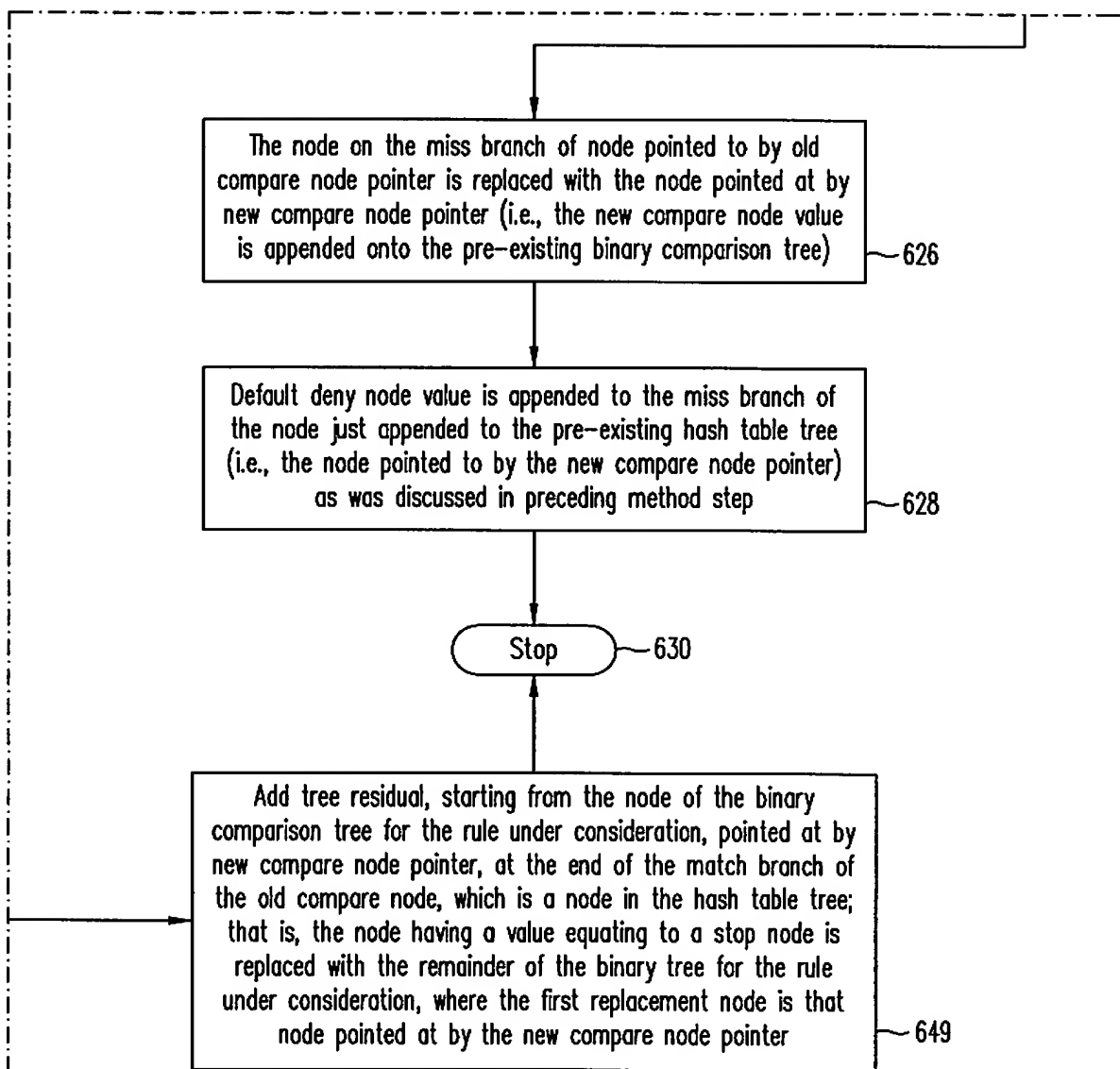


FIG. 6F

FIG. 6A	FIG. 6B	FIG. 6C
FIG. 6D	FIG. 6E	FIG. 6F

Key To
 FIG. 6

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Simplified Example of Ordered ACL Rule Set Typically Entered by a Network Administrator	
ACL Rules in an Ordered ACL Rule Set expressed as plain english statements	Examples of Coded Versions of ACL Rules Which Are Typically Utilized Within an ACL Rule Set
Permit TCP protocol packets from any source IP address going to host having an IP address of 28.16.31.10 and a port identifier equal to 28.	PERMIT TCP ANY HOST 28.16.31.10 EQ 28
Deny TCP protocol packets from any source IP address going to host having an IP address of 28.16.31.10 and a port identifier greater than 23.	DENY TCP ANY HOST 28.16.31.10 GT 23
Deny UDP protocol packets from any source IP address going to host having an IP address of 30.22.12.5 and a port identifier equal to 11.	DENY UDP ANY HOST 30.22.21.5 EQ 11
Permit UDP protocol packets from any source IP address going to host having an IP address of 30.22.12.X, where X indicates any number, or "don't care".	PERMIT UDP ANY HOST 30.22.21.X
Deny all packets having source IP address of 23.20.7.0 and any destination address (indicated by address X.X.X.X, where X indicates any number, or "don't care").	DENY TCP 23.20.7.0 X.X.X.X
Permit TCP protocol packets from any source IP address going to host having an IP address of 28.16.32.10.	PERMIT TCP ANY HOST 28.16.31.10

FIG. 7A

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Example of the Creation of a Bit Selection Vector

"0" Count in Each Bit Position: "X" Count in Each Bit Position: Total of "0" + "X" Counts:	40440.01000.01010.11000.11111.00035.05335.02020.41313.11111 00000.55555.55555.55555.55555.11111.11111.22222.33333 40440.56555.56555.66555.66666.11146.16446.13131.63535.44444
"1" Count in Each Bit Position: "X" Count in Each Bit Position: Total of "1" + "X" Counts:	26226.10111.10101.00111.00000.55520.50220.53535.03131.22222 00000.55555.55555.55555.55555.11111.11111.22222.33333 26226.65666.65666.55666.55555.66631.61331.64645.25353.55555 46446.66666.66666.66666.66666.66646.66446.64645.65555.55555
Construct a "Larger Total Count" row having one row entry corresponding to each bit position in the strings which were constructed from the ACL rules; fill each row entry with the larger of either the "Total of '0' + 'X' Counts" or "Total of '1' + 'X' Counts" for the bit position corresponding to that row entry.	
Construct a "Smaller Total Count" row having one row entry corresponding to each bit position in the strings which were constructed from the ACL rules; fill each row entry with the smaller of either the "Total of '0' + 'X' Counts" or "Total of '1' + 'X' Counts" for the bit position corresponding to that row entry.	20226.55555.55555.55555.55555.11131.11331.13131.23333.44444
Set number of unspecified pointers of bit selection vector = specified bit length of hash table index	Number of Unspecified Pointers of Bit Selection Vector = 4 For sake of example, assume hash table index having a bit length of 4 is specified.
Select the row entries in the "Larger Total Count" Row columns having the smallest number entries; designate the bit positions corresponding to the selected row columns as potential, "P", candidate columns which might be utilized as the pointers of the bit selection vector	P PP P PP P P P Note: The row columns 1, 3, 34, 39, 41, and 46 of the "Larger Total Count" row had the smallest entries (i.e., the base 10 number "4"), and thus the bit positions associated with row columns 1, 3, 34, 39, 41, and 46 of the "Larger Total Count" row are designated as potential candidate bits "P."
Since there are more potential, "P", candidate columns than number of unspecified pointers of bit selection vector, refine the selection by examining the columns of the Smaller Total	R RR Note: The row columns 1, 3, and 4 of the "Smaller Total Count" row, corresponding with the selected row columns of the "Larger Total Count" row, had the smallest entries (i.e., the base 10 number "2"), and thus the bit positions associated with row columns 1, 3, and 4 of the "Smaller Total Count" row are redesignated as

FIG. 7C1

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Count Row, with such examined Smaller Total Count Row columns being those corresponding to the Larger Total Count Row columns designated as potential, "P," candidate columns; redesignate as potential, "R," candidate columns which might be utilized as the pointers of the bit selection vector, those examined Smaller Total Count Row columns with the smallest number entries	candidate bits "R".
Since the number or redesignated potential candidates, "R," is less than the number of unspecified pointers of bit selection vector, designate all redesignated, "R," candidates as actual, "K," bit selection vector Pointer Indication Columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector	<p>K K</p> <p>Note: The number of unspecified pointers of bit selection vector is currently equal to 4, and the number of redesignated potential candidates, "R," is 3, which is less than the number of unspecified pointers of bit selection vector; thus, all "R" potential candidates are specified actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector.</p>
Subtract the number of specified actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector, from number of unspecified pointers of bit selection vector	<p>Number of unspecified pointers of bit selection vector =</p> <p>number of unspecified pointers of bit selection vector (i.e., 4) -</p> <p>number of specified actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector specified in preceding step (i.e., 3)</p> <p>= 1 pointer left unspecified</p>
Since the number of unspecified pointers of bit selection vector is still non-zero, mark specified "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by	<p>* **</p> <p>Note: Row columns 1, 3, and 4 are marked with asterisks to indicate that since these row columns have already been designated as candidates "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector.</p>

FIG. 7C2

<p>pointers of the bit selection vector with asterisks indicating that such columns are no longer selectable or under consideration, since the bit positions associated with the "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector have already been specified</p>	
<p>Thereafter, repeat the "select the row entries in the Larger Total Count" Row having smallest number entries . . . " operation above upon the row columns which have not yet been designated as candidate "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector</p>	<div> <div>* **</div> <div> <div>P</div> <div>PP</div> <div>PP</div> <div>P</div> </div> <div> <p>Note: Row columns 1, 3, and 4 are marked with asterisks to indicate that since the bit positions associated with these row columns have already been designated as candidates.</p> </div> </div>

FIG. 7C3

<p>Since there are more candidates, "P," than number of unspecified pointers of bit selection vector (at this point, 3 pointers have been specified as "K," meaning that one additional pointer is necessary to have the pointers required to completely point out the 4 bit hash table index), repeat the refine the selection operation above</p>	<p>R RR R R</p> <p>Note: Since all entries in the "Smaller Total Count" Row columns, corresponding with the selected row columns of the "Larger Total Count" Row, were the same number (i.e., the base ten number "3"), all P row columns are redesignated as candidates "R".</p>
<p>Since after redesignation there are still more candidates "R" than the number of unspecified pointers of bit selection vector, all "R," candidates are deemed equally good choices; consequentially, the number of actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector necessary to completely point out the hash table index value (i.e., in the present example, one more pointer is needed) may be selected at random from the designated "R" row columns.</p>	<p>K</p> <p>Note: Select row column 34 at random.</p>
<p>There are now specified actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector equal in number to the bit length of the hash table index; consequently, all pointers of the bit selection vector, which will be utilized to point to bit positions used to form a hash table index value which will be used to "key into"</p>	<p>K KK</p> <p>Note: These actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector indicate that the first, third, and fourth leftmost bit positions within the "protocol ID" field, and the fourth leftmost bit positions within the "destination address" field will be utilized as the hash table index bits.</p>

FIG. 7C4

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the hash table, have been fully specified.	
Definition of the bit selection vector	Bit Selection Vector = (pointer to first leftmost bit position within the "protocol ID" field; pointer to third leftmost bit position within the "protocol ID" field; pointer to fourth leftmost bit position within the "protocol ID" field; pointer to fourth leftmost bit position within the "destination address" field)

FIG. 7C5

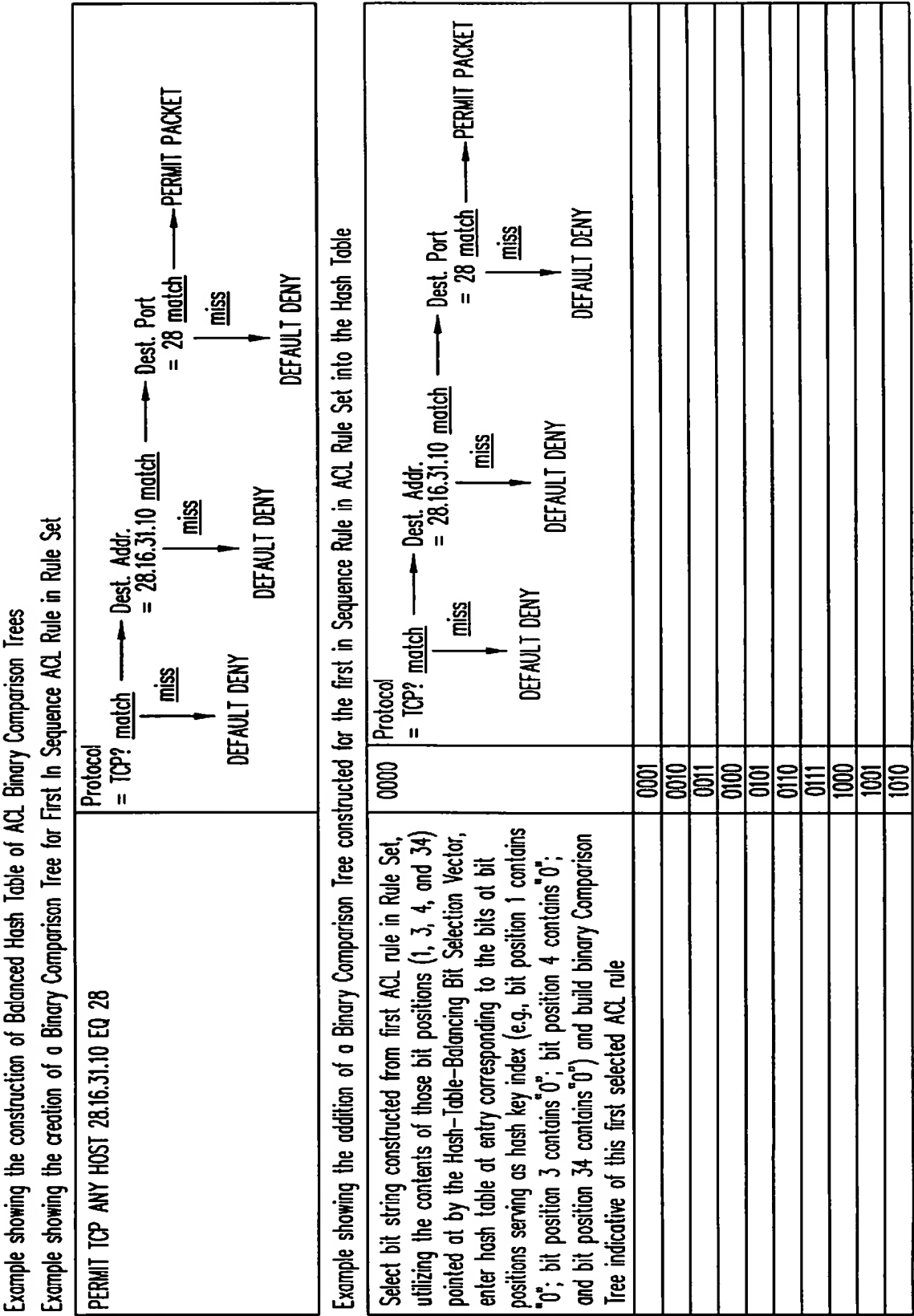


FIG. 7D1

	1011	
	1100	
	1101	
	1110	
	1111	

FIG. 7D2

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Example Showing the Construction of Balanced Hash Table of ACL Binary Comparison Trees (cont.)
 Example Showing the Creation of a Binary Comparison Tree for Second In Sequence Rule in Rule Set

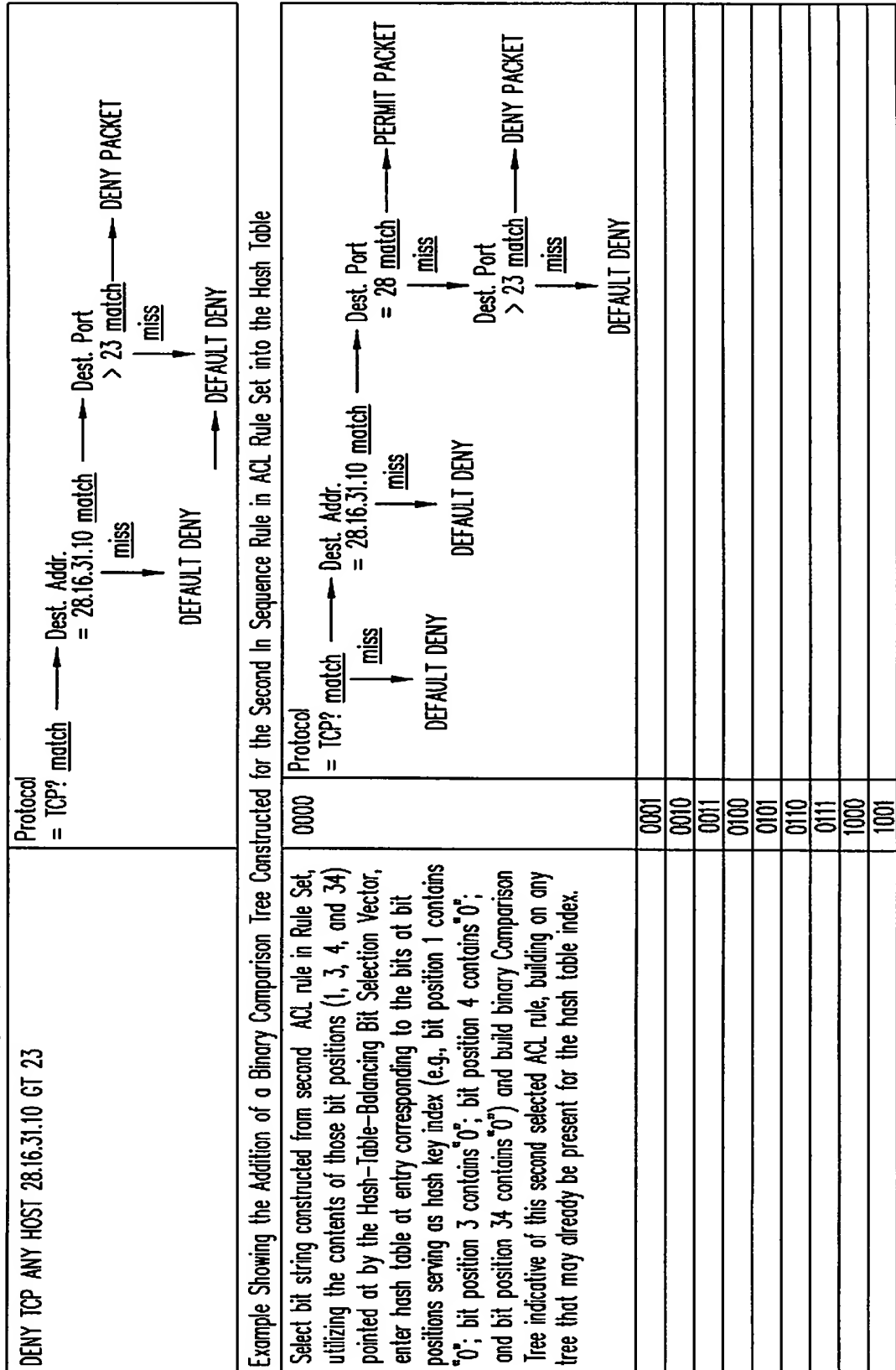


FIG. 7D3

	1010	
	1011	
	1100	
	1101	
	1110	
	1111	

FIG. 7D4

Example Showing the Construction of Balanced Hash Table of ACL Binary Comparison Trees (cont.)
Example Showing the Creation of a Binary Comparison Tree for Third In Sequence ACL Rule in Rule Set

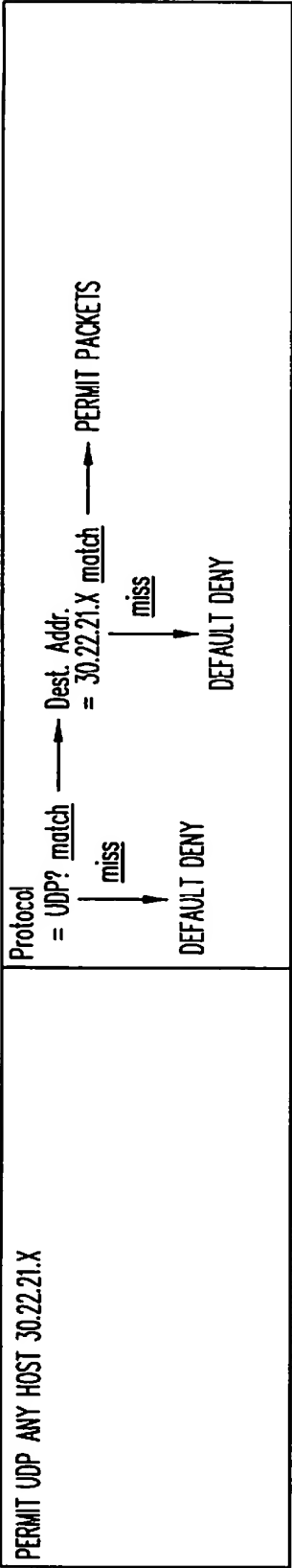
DENY UDP ANY HOST 30.22.21.5 EQ 11	<div>Protocol = UDP? match → Dest. Addr. = 30.22.21.5 match → Dest. Port = 11 match → DENY PACKET</div> <div>miss → DEFAULT DENY</div>
Example Showing the Addition of a Binary Comparison Tree Constructed for the Third In Sequence Rule in ACL Rule Set into the Hash Table	
	<div>Protocol = TCP? match → Dest. Addr. = 28.16.31.10 match → Dest. Port = 28 match → PERMIT PACKET</div> <div>miss → DEFAULT DENY</div> <div>Dest. Port > 23 match → DENY PACKET</div> <div>miss → DEFAULT DENY</div>

FIG. 7D5

	0110	
	0111	
	1000	
	1001	
	1010	
	1011	
	1100	
	1101	
	1110	
<p>Select bit string constructed from third ACL rule in Rule Set, utilizing the contents of those bit positions (1, 3, 4, and 34) pointed at by the Hash-Table-Balancing Bit Selection Vector, enter hash table at entry corresponding to the bits at bit positions serving as hash key index (e.g., bit position 1 contains "1"; bit position 3 contains "1"; bit position 4 contains "1"; and bit position 34 contains "1") and build binary Comparison Tree indicative of this third selected ACL rule</p>	1111	<p>Protocol = UDP? <u>match</u> → Dest. Addr. = 30.22.21.5 <u>match</u> → Dest. Port = 11 <u>match</u> → DENY PACKET</p> <p><u>miss</u> → DEFAULT DENY</p> <p><u>miss</u> → DEFAULT DENY</p> <p><u>miss</u> → DEFAULT DENY</p>

FIG. 7D6

Example Showing the Construction of Balanced Hash Table of ACL Binary Comparison Trees (cont.)
Example Showing the Creation of a Binary Comparison Tree for Fourth In Sequence ACL Rule in Rule Set



Example Showing the Addition of a Binary Comparison Tree Constructed for the Fourth In Sequence Rule in ACL Rule Set into the Hash Table

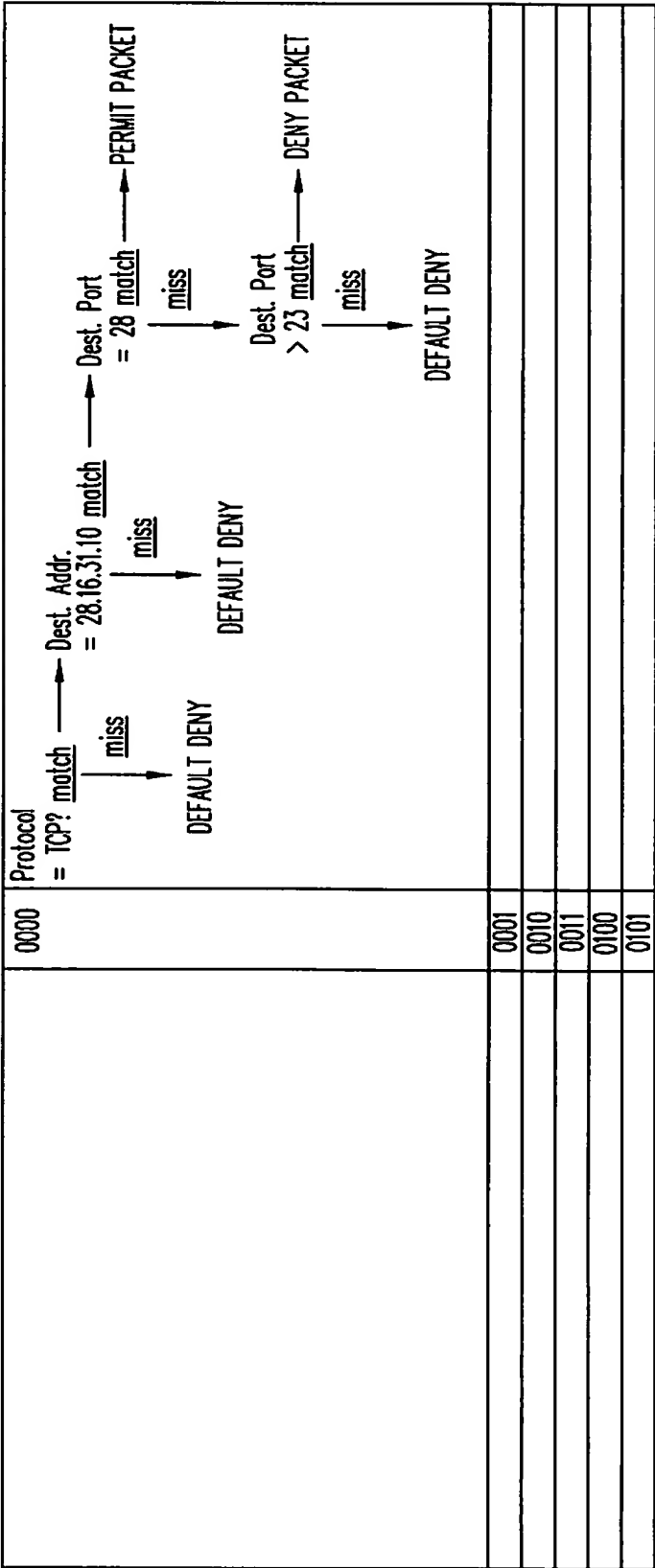


FIG. 7D7

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	0110		
	0111		
	1000		
	1001		
	1010		
	1011		
	1100		
	1101		
	1110		
	1111	<p>Select bit string constructed from fourth ACL rule in Rule Set, utilizing the contents of those bit positions (1, 3, 4, and 34) pointed at by the Hash-Table-Balancing Bit Selection Vector, enter hash table at entry corresponding to the bits at bit positions serving as hash key index (e.g., bit position 1 contains "1"; bit position 3 contains "1"; bit position 4 contains "1"; and bit position 34 contains "1") and build binary Comparison Tree indicative of this fourth selected ACL rule, building on any tree that may already be present for the hash table index</p>	<p>Protocol = UDP? <u>match</u> → Dest. Addr. = 30.22.21.5 <u>match</u> → Dest. Port = 11 <u>match</u> → DENY PACKET</p> <p><u>miss</u> → DEFAULT DENY</p> <p><u>miss</u> → 30.22.21.X <u>match</u> → PERMIT PACKET</p> <p><u>miss</u> → DEFAULT DENY</p>

FIG. 7D8

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<p>Select bit string constructed from fifth ACL rule in Rule Set, utilizing the contents of those bit positions (1, 3, 4, and 34) pointed at by the Hash-Table-Balancing Bit Selection Vector, enter hash table at entry corresponding to the bits at bit positions serving as hash key index (e.g., bit position 1 contains "0"; bit position 3 contains "0"; bit position 4 contains "0"; and bit position 34 contains "X") and build binary Comparison Tree indicative of this fifth selected ACL rule, building on any tree that may already be present for the hash table index; however, since bit at bit position 34 is X, the rule will be appended at both 0000 and 0001, since bit position 34 may be either 0 or 1.</p>	0001	<p>Protocol = TCP? <u>match</u> → Source Addr. = 23.20.7.0 <u>match</u> → DENY PACKETS</p> <p><u>miss</u> → DEFAULT DENY</p>
	0010	
	0011	
	0100	
	0101	
	0110	
	0111	
	1000	
	1001	
	1010	
	1011	
	1100	
	1101	
	1110	
	1111	<p>Protocol = UDP? <u>match</u> → Dest. Addr. = 30.22.21.5 <u>match</u> → Dest. Port = 11 <u>match</u> → DENY PACKET</p> <p><u>miss</u> → DEFAULT DENY</p> <p><u>miss</u> → 30.22.21.X <u>match</u> → PERMIT PACKET</p> <p><u>miss</u> → DEFAULT DENY</p>

FIG. 7D10

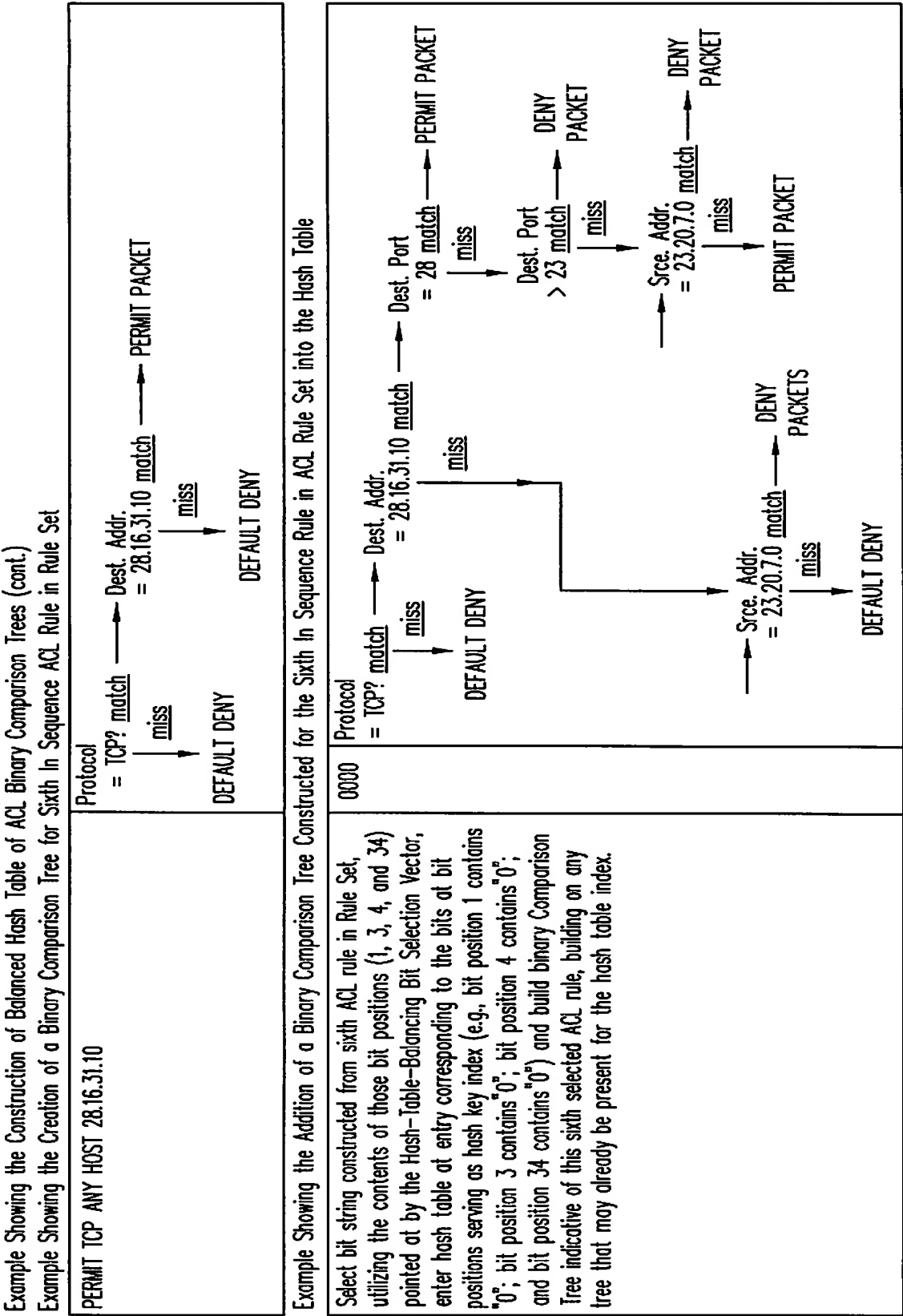


FIG. 7D11

	0001	
	0010	
	0011	
	0100	
	0101	
	0110	
	0111	
	1000	
	1001	
	1010	
	1011	
	1100	
	1101	
	1110	
	1111	<div> <div>Protocol = UDP?</div> <div> <div>match</div> <div>miss</div> </div> <div> <div>Dest. Addr. = 30.22.21.5</div> <div>match</div> <div>miss</div> </div> <div> <div>Dest. Port = 11</div> <div>match</div> <div>miss</div> </div> <div> <div>DENY PACKET</div> <div>miss</div> <div>DEFAULT DENY</div> <div>PERMIT PACKET</div> <div>miss</div> <div>DEFAULT DENY</div> </div> </div>

FIG. 7D12